

CURRICULUM VITAE

VIDYA SAGAR YADAV
S/O Mr. RAMNAGINA
YADAV

VILL- PIDARI,
POST- KORAYA
DIST- DEORIA
U.P.

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CAREER OBJECTIVE

Seeking a challenging position where the knowledge I have gained can be utilized for the development of the organization. Aim is to maximize and enhance my skills to better serve for the progress of the organization.

4 : CURRENTLY WORKING

Working with RD Electronics Oversees Pvt. Ltd. plot no 16 sec 3. Sidkul Haridwar Uttarakhand. From march 2021 to Till now. As a **Production Engineer**.

Work responsibility:

- Morning meeting & rejection book offer to QA
- Maintain 5s tools in production area for better efficiency.
- Set up check sheet review and approval.
- Supervising & Operating Technology.
- Solving Problems & Administrative Planning.
- Handling SMT Line, SMD Line and Packing Line
- PCB Soldering and Mounting in (SMD).
- Wave Soldering and mounting.
- Line documents update & Trainee record update
- Display board update review and approval
- 4M change record. As per new system
- Maximum utilization of machinery to achieve daily production targets.
- Continuously line checking in order to maintain quality & rejection control.
- Controlling of production as per planning given by PPC.
- Inventory Control of production area.
- Maintaining daily production & machine related records.
- Frequent machine planning to ignore any bottle neck situation in production line.

3: WORKED EXPERIENCE

Worked with Rosmerta Technologies Limited. Manesar Gurgoan. As **Production Engineer**. From June 2020 to Feb 2021.

2: WORKED EXPERIENCE

Worked with Hella Electronics India Pvt. Ltd. (M.N.C) Basai Dhankot Gurgoan from Jan 2018 to May 2020. As **Tanning & Production Engineer**.

1:WORKED EXPERIENCE

Worked with Hi Pad Technologies Pvt. Ltd. sec 63 noida. From June 2017 to Jan 2018. As a **Production Engineer**.

SOFTWARE SKILLS

- **Basic PLC, HMI Programming & Wiring.**
- Delta plc in Ladder Logic & Mitsubishi plc in Ladder Logic.
- Allen Bradley plc in Ladder Logic & HMI Programming in Mitsubishi, Delta.
- Basic PCB circuit designing with Easyeda Software.
- SAP knowledge in Production.
- Computer knowledge MS Office, Excel etc.

EDUCATIONAL QUALIFICATION

Qualification	Board/university	Year	percentage
Diploma[Electronics engineering]	BTEUP	2014-2017	75%
Graduation	D D U Gorakhpur UP	2012-2015	51.3%
Intermediate	UP BOARD	2012	58.5%
High school	UP BOARD	2009	60.5%

HOBBIES:

- Roaming
- Reading books

STRENGTH:-

- Ability to grab things quickly
- Committed on responsibility

PERSONAL DETAIL:-

Name : Vidya Sagar Yadav

Father's Name : Mr. Ramnagina Yadav :

Mother's Name : Smt. Meera Devi

Date of Birth : 10th Dec 1994

Gender : Male

Marital Status : Unmarried

Nationality : Indian

Languages Known : Hindi, English

DECLARATION: -

I hereby declare that all the information above are correct to the best of my knowledge.

Place: Gorakhpur (U P)

Date: (Vidya Sagar Yadav)