**RESUME**

**Mr.**

**Khamkar Abhishek Sayajirao**

**Permanent address:**

90/2518, Kannamwar Nagar-2,

Vikhroli (East), Mumbai – 400083, Maharashtra.

**Current address:**

Flat No. 10, AL 5-17,

Sneha Sagar Apartment, Sector-17,

Airoli, Navi Mumbai- 400708, Maharashtra.

**E-mail:** abhishek.khamkar1398@gmail.com

**Contact No:** 9619776928 / 9372965251

**Educational Qualification:** B.E. (Electrical)

**Objective**

 Looking for challenging and Responsible Assignment, which include professional excellence and value addition to my career through dedicated work, creativity and team work.

**Personnel Details**

|  |  |
| --- | --- |
| Date of Birth: | 01st March 1998 |
|  Languages Known: | English, Hindi, Marathi |
| Father: | Mr.Sayajirao Damodar Khamkar |
| Mother: | Mrs.Rajnee Sayajirao Khamkar |

|  |  |
| --- | --- |
| Age: | 22 |
| Sex: | Male |
| Marital Status: | Single |

 **Educational Qualification**

**Secondary & Higher Secondary Education**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Exam | School/College | Board | Year | Percentage | Class |
| S.S.C. | Vikas High School,Vikhroli,Mumbai | Maharashtra State Board, Mumbai | 2014 | 85.40 % | Distinction |
| Diploma | K.J. Somaiya Polytechnic,Mumbai | Autonomous Institute | 2017 | 84.20 % | Distinction |

**Educational Qualification Degree**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Exam | College | Board | Year | CGPA | Class |
| B.E. | A.C. Patil College Of Engineering | Mumbai University | 2020 | 7.76 | Distinction |

**INTERNSHIP**

|  |  |
| --- | --- |
| Internship At | Duration |
| Mai Electrical Services ,B-11 Yashoda Residency,sadashiv Dangat Nagar,Ambegaon, Pune-411046. | 21st June 2019 to 9th July 2019 |

**PROJECT DETAILS**

* **Diploma Project 2017**

|  |  |
| --- | --- |
| Project Title | Objective |
| Underground Cable Fault Detector | To find exact location of the fault for underground cables. |

* **Degree Project 2020**

|  |  |
| --- | --- |
| Project Title | Objective |
| Single Phase PWM Rectifier | To improve power factor and harmonic reduction of input. |

**EXTRA CURRICULAR ACTIVITIES**

* Passed “ELEMENTARY GRADE DRAWING EXAMINATION” held in the year 2009 with grade B.
* Passed “INTERMEDIATE GRADE DRAWING EXAMINATION” held in the year 2011 with grade A.
* Participated in I.T. Champs 2012-2013 Competition.
* Participated in District level Project Competition in the year 2012.
* Participated in State level “Dipex 2017” Project Competition.

**TECHNICAL SKILL**

|  |  |
| --- | --- |
| Operating Systems | Windows XP, Windows 7, Windows 10 |
| Other Skill | AutoCad, MATLAB, Ms-Office and Good Computer Handling Knowledge  |

**DECLARATION**

I hereby decided that the information furnished above is true to the best my knowledge.

(Khamkar Abhishek Sayajirao)